

Academic Staff College(ASC)

In association with School of Information Technology and Engineering (SITE)

Cordially invites you to



Faculty Coordinator : Dr. Iyapparaja M

Backend Implementation of VLSI Circuits

Resource Person :

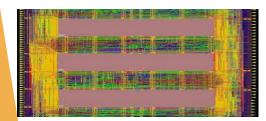


Mr ETB Samuel Jigme Harrison, Semiconductor Professional, Senior IEEE Member, Singapore.

Discussion Points :

Introduction to Semiconductors & VLSI circuits

- Introduction to Physical Design
- Introduction to Innovus
- ✤ Floorplanning
- Power Planning
- Placement
- Clock Tree Synthesis
- Routing
- Static Timing Analysis
- Physical Verification
- Power Analysis
- Life in VLSI



10 February - 2023 (Friday)@ 10:00AM-5:00PM

Please register through your vtop login at https://vtop.vit.ac.in/vtop/initialProcess